Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the instant application:

Listing of Claims:

1. (Currently Amended) An integrated circuit, comprising:

at least one DC to DC converter including structure for receiving a supply voltage and structure for producing a plurality of output supply voltages ~~including at least one output voltage~~ at a greater voltage level than said supply voltage; and

processing circuitry having structure for receiving at least one of said plurality of output supply voltages as its supply voltage and at least one time-varying input signal, said processing circuit modifying a parameter of said time-varying signal to produce [[a]] at least one modified time-varying output signal, ~~said processing circuit having a plurality of outputs,~~ wherein an output voltage level of each of ~~a first of~~ said at least one modified output[[s]] signal is scaled to ~~greater than~~ an output voltage level of a ~~second of said~~ received one of said plurality of output[[s]] supply voltages.

2. (Previously Presented) The integrated circuit of claim 1, wherein said time-varying input signal comprises an RF, microwave or a digital signal.

3. (Original) The integrated circuit of claim 1, wherein said parameter is selected from the group consisting of a voltage level and a frequency.

4. (Previously Presented) The integrated circuit of claim 1, wherein said DC to DC converter includes structure for receiving opposite phase clock signals, said clock signals toggling between said supply voltage and ground.

5. (Original) The integrated circuit of claim 1, wherein said processing circuitry comprises digital circuitry.

6. (Original) The integrated circuit of claim 1, wherein said processing circuitry comprises analog circuitry.

7. (Original) The integrated circuit of claim 1, wherein said processing circuitry comprises analog and digital circuitry.

8. (Original) The integrated circuit of claim 1, wherein said time-varying input signal is a digital signal.

9. (Original) The integrated circuit of claim 1, wherein said time-varying input signal is an analog signal.

10. (Original) The integrated circuit of claim 1, wherein said parameter of said time-varying signal that is modified by said processing circuitry is programmable.

11. (Original) The integrated circuit of claim 1, wherein said processing circuitry comprises an input buffer and an output buffer.

12. (Currently Amended) The integrated circuit of claim 1, further comprising at least one passive element for providing programmability to said at least one ~~intermediate voltage~~ of said plurality of output supply voltages.

13. (Original) The integrated circuit of claim 12, wherein said at least one passive element is a peripheral passive element.

14. (Original) The integrated circuit of claim 1, wherein said DC to DC converter is switched capacitor based.

15-16. Cancelled

17. (Currently Amended) A circuit board, comprising:

a plurality of integrated circuits disposed on said board, said plurality of integrated circuits collectively requiring a plurality of different supply voltage levels and signals at respective inputs for operation; and

an integrated power supply circuit disposed on said board, said integrated power supply circuit comprising:

at least one DC to DC converter including structure for receiving a supply voltage and structure for producing a plurality of output supply voltages ~~including at least one output voltage~~ at a greater voltage level than said supply voltage, more than one of said plurality of output supply voltages coupled to said plurality of integrated circuits, and;

processing circuitry having structure for receiving at least one of said plurality of output supply voltages as its supply voltage and at least one time-varying input signal, said processing circuit modifying a parameter of said time-varying signal to produce [[a]] at least one modified time-varying output signal, said at least one modified time-varying output signal of said processing circuit ~~having a plurality of outputs~~ being coupled to said inputs of said plurality of integrated circuits, wherein an output voltage level of ~~a first of~~ said at least one modified output[[s]] signal is scaled to ~~greater than~~ an output voltage level of ~~a second of~~ said one of said plurality of output[[s]] supply voltages.

18. (Previously Presented) The circuit board of claim 17, wherein said time-varying input signal comprises an RF, microwave or a digital signal.

19. (Original) The circuit board of claim 17, wherein said parameter is selected from the group consisting of a voltage level and a frequency.

20. (Previously Presented) The circuit board of claim 17, wherein said DC to DC converter includes structure for receiving opposite phase clock signals, said clock signals toggling between said supply voltage and ground.

21. (Original) The circuit board of claim 17, wherein said processing circuitry comprises digital circuitry.

22. (Original) The circuit board of claim 17, wherein said processing circuitry comprises analog circuitry.

23. (Original) The circuit board of claim 17, wherein said processing circuitry comprises analog and digital circuitry.

24. (Original) The circuit board of claim 17, wherein said time-varying input signal is a digital signal.

25. (Original) The circuit board of claim 17, wherein said time-varying input signal is an analog signal.

26. (Original) The circuit board of claim 17, wherein said parameter of said time-varying signal that is modified by said processing circuitry is programmable.

27. (Original) The circuit board of claim 17, wherein said processing circuitry comprises an input buffer and an output buffer.

28 Cancelled

29. Cancelled

30. (Original) The circuit board of claim 17, wherein said DC to DC converter is switched capacitor based.

31. Cancelled